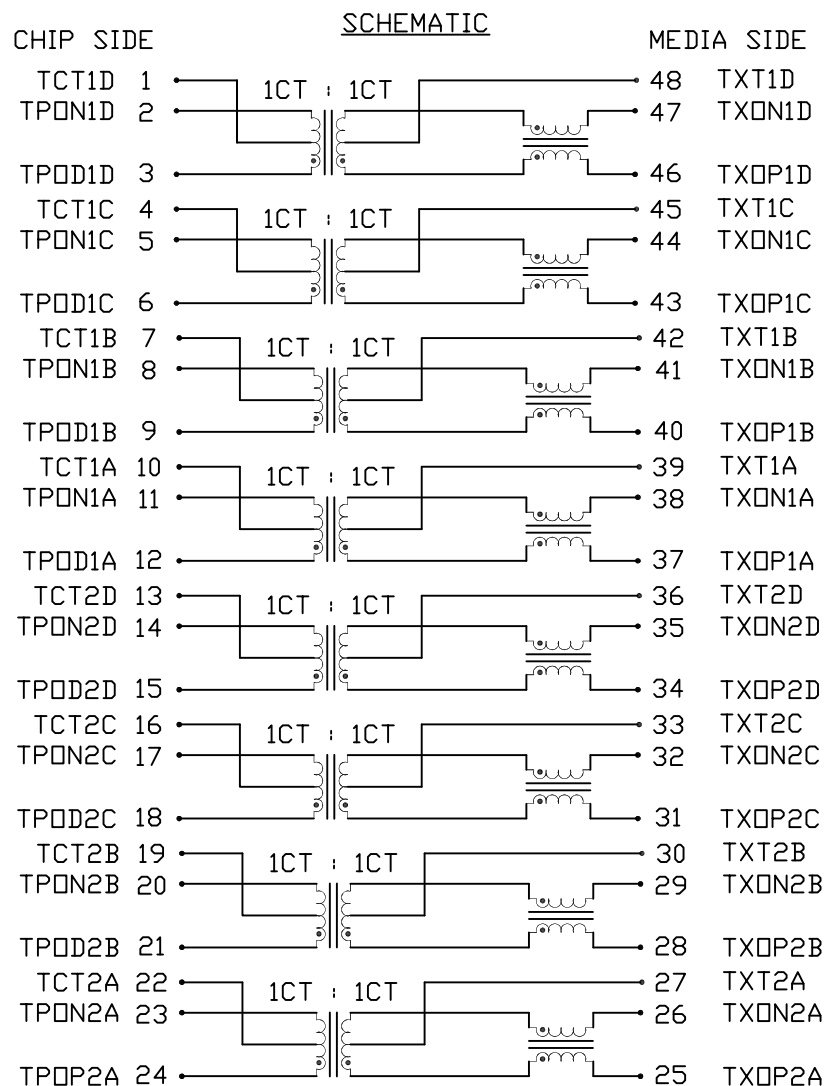


THE INFORMATION CONTAINED HEREIN IS CONSIDERED 'PROPRIETARY' TO BEL FUSE INC. AND SHALL NOT BE COPIED, REPRODUCED OR DISCLOSED WITHOUT THE WRITTEN APPROVAL OF BEL FUSE INC.

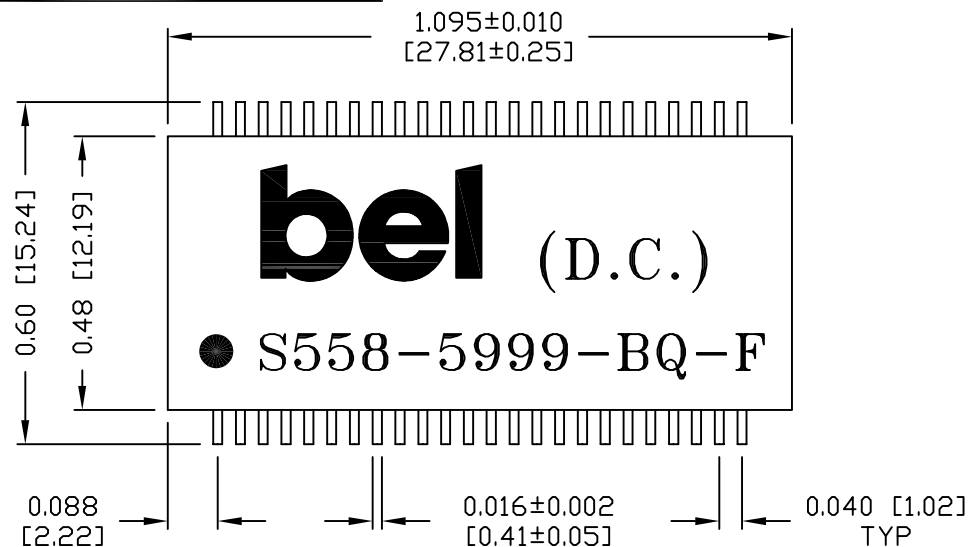


ELECTRICAL CHARACTERISTICS @ 25°C

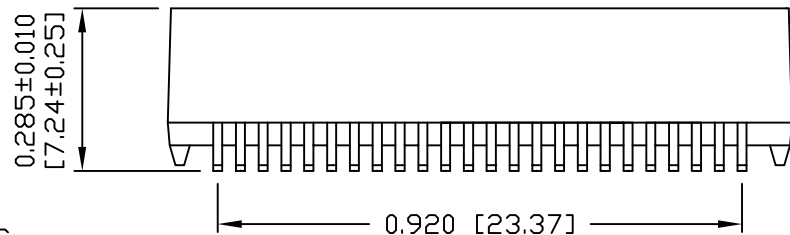
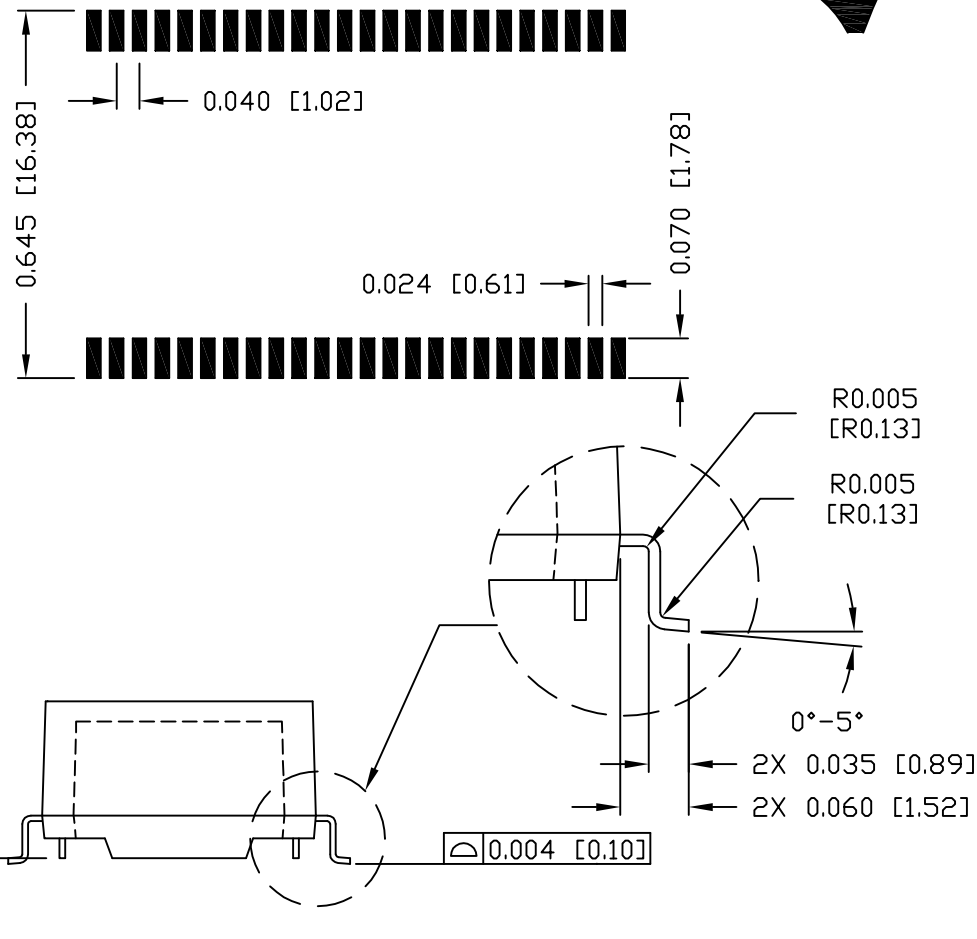
TURNS RATIO	CHIP SIDE : MEDIA SIDE	1CT : 1CT
POLARITY	PER DOT CONVENTION	
OCL	350μH MIN @100kHz, 100mV WITH 8mA DC BIAS PIN (46-47), (43-44), (40-41), (37-38) PIN (34-35), (31-32), (28-29), (25-26)	
INSERTION LOSS	0.9dB MAX @1-100MHZ 1.5dB MAX @125MHZ	
RETURN LOSS	18dB MIN @0.5 TO 40MHZ MEASURED UNDER 12-20LOG(f/80MHZ)dB MIN @40 TO 100MHZ 100 Ω ENVIRONMENT	
COMMON TO COMMON MODE REJECTION	30dB MIN @0.3 TO 100MHZ	
CROSSTALK	33-20LOG(f/100MHZ)dB MIN @0.3 TO 100MHZ	
HIPOT	LEAKAGE CURRENT LESS THAN 1mA AT 1500VAC	
OPERATING TEMPERATURE	-40°C TO +85°C	

ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM.	[ ] METRIC DIM. AS REF.	
Alice Pang	2021-01-27	ELECTRICAL SPECIFICATION S558-5999-BQ-F	S558-5999-BQ-F	TOL. IN INCH	UNIT : INCH [mm]	
DRAWN BY	DATE		FILE NAME	.X	SCALE : N/A	
ZC Guo	2021-01-27		S5585999BQFA.DWG	.XX	SIZE : A4	
				.XXX		

THE INFORMATION CONTAINED HEREIN IS CONSIDERED  
 'PROPRIETARY' TO BEL FUSE INC. AND SHALL NOT BE  
 COPIED, REPRODUCED OR DISCLOSED WITHOUT THE  
 WRITTEN APPROVAL OF BEL FUSE INC.



SUGGESTED PCB PAD LAYOUT



NOTES:

- STANDARD MARKING REFER TO DOC. HAND-WORK-04.
- PACKAGE CODE: "QVS001".

ORIGINATED BY	DATE
Lawrence Tsang	2021-01-27
DRAWN BY	DATE
ZC Guo	2021-01-27

TITLE
MECHANICAL OUTLINE
S558-5999-BQ-F

PART NO. / DRAWING NO.
S558-5999-BQ-F
FILE NAME
S5585999BQFA.DWG

STANDARD DIM.	[ ] METRIC DIM. AS REF.
TOL. IN INCH	UNIT : INCH [mm]
.X	SCALE : N/A
.XX ±0.01	SIZE : A4
.XXX ±0.005	

REV. : A	PAGE : 3